

Amendments to the Claims:

Prior to continued examination, please enter the following amendments to the claims without prejudice, without admission, without surrender of subject matter, and without any intention of creating any estoppel as to equivalents as follows. This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (previously presented) A non-volatile semiconductor memory device comprising:

a plurality of memory cell arrays each having a plurality of memory cells, said plurality of memory cells being connected to a plurality of word lines;

a plurality of word line drive circuits which are connected to said plurality of memory cell arrays, and which select and drive said plurality of word lines of said corresponding memory cell arrays; and

a plurality of control circuits which are connected to said plurality of memory cell arrays, and execute verifying reading of data which has been written in said plurality of memory cells of the corresponding memory cell arrays, and control a select and drive operation for said word lines in the corresponding word line drive circuit in accordance with a result of the verifying reading.

Claims 2-31 (canceled).